

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: **Kozo SHIMIZU et al.**

Serial Number: **Not Yet Assigned**

Filed: **February 5, 2004**

For: **METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE**

Attorney Docket No. **042080**

Customer No.: **38834**

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

February 5, 2004

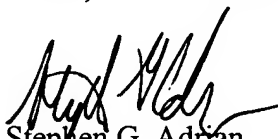
Sir:

In compliance with 37 CFR 1.56, Applicants call to the attention of the Patent and Trademark Office the references listed on the attached PTO-1449.

A copy of each of the references is enclosed herewith.

In the event there are any fees due in connection with the filing of this paper, please charge Deposit Account No. 50-2866.

Respectfully submitted,
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Enclosures: PTO-1449; References (2)

INFORMATION DISCLOSURE STATEMENT PTO-1449	Attv. Docket No. 042080	Serial No. New Appln.
	Applicant(s): Kozo SHIMIZU et al.	
	Filing Date: February 5, 2004	Group Art Unit:

U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
_____	AA					
_____	AB					
_____	AC					
_____	AD					

FOREIGN PATENT DOCUMENTS

Document No.	Date	Country	Translation (Yes or No)
_____ AE 6-13382	01/21/94	Japan	Abstract. Discussed in the spec.
_____ AF 2000-260819	09/22/00	Japan	Abstract.
_____ AG			
_____ AH			
_____ AI			

OTHER DOCUMENTS

_____ AJ	
_____ AK	
Examiner	Date Considered